

**IN THE CLAIMS:**

1. (Canceled)
2. (Canceled)
3. (Canceled)
4. (Canceled))
5. (Previously presented) A method for reporting failures, comprising:  
detecting a predetermined number of consecutive correctable errors;  
storing a description for each of the predetermined number of correctable errors in an error data structure;  
determining whether the descriptions for the predetermined number of correctable errors are the same;  
reporting a bit line or driver failure if the descriptions for the predetermined number of correctable errors are the same; and  
clearing the error data structure if a correctable error is not encountered on an event scan call before detecting the predetermined number of consecutive correctable errors.
6. (Canceled)
7. (Canceled)
8. (Previously presented) A method for reporting failures, comprising:  
detecting a predetermined number of consecutive correctable errors;  
storing a description for each of the predetermined number of correctable errors, wherein each description comprises an address at which an error occurred and an error signature that indicates which bit is bad;

determining whether the descriptions for the predetermined number of correctable errors are the same; and

reporting a bit line or driver failure if the descriptions for the predetermined number of correctable errors are the same.

9. (Previously presented) A method for reporting failures, comprising:  
detecting a predetermined number of consecutive correctable errors;  
storing a description for each of the predetermined number of correctable errors;

determining whether the descriptions for the predetermined number of correctable errors are the same;

reporting a bit line or driver failure if the descriptions for the predetermined number of correctable errors are the same; and

deconfiguring a processor if the descriptions for the predetermined number of errors are the same.

10. (Original) The method of claim 9, wherein the step of deconfiguring the processor comprises dynamically deconfiguring the processor.

11. (Original) The method of claim 9, wherein the step of deconfiguring the processor comprises deconfiguring the processor at boot time.

12. (Previously presented) A method for reporting failures, comprising:  
detecting a predetermined number of consecutive correctable errors;  
storing a description for each of the predetermined number of correctable errors;

determining whether the descriptions for the predetermined number of correctable errors are the same;

reporting a bit line or driver failure if the descriptions for the predetermined number of correctable errors are the same; and

replacing a processor if the descriptions for the predetermined number of correctable errors are the same.

13. (Canceled)

14. (Canceled)

15. (Canceled)

16. (Canceled)

17. (Previously presented) An apparatus for reporting failures, comprising:  
detection means for detecting a predetermined number of consecutive correctable errors;

an error data structure for storing a description for each of the predetermined number of correctable errors;

determination means for determining whether the descriptions for the predetermined number of correctable errors are the same;

reporting means for reporting a bit line or driver failure if the descriptions for the predetermined number of correctable errors are the same; and

means for clearing the error data structure if a correctable error is not encountered on an event scan call before detecting the predetermined number of consecutive correctable errors.

18. (Canceled)

19. (Canceled)

20. (Previously presented) An apparatus for reporting failures; comprising:  
detection means for detecting a predetermined number of consecutive correctable errors;

storage means for storing a description for each of the predetermined number of correctable errors, wherein each description comprises an address at which an error occurred and an error signature that indicates which bit is bad;

determination means for determining whether the descriptions for the predetermined number of correctable errors are the same; and

reporting means for reporting a bit line or driver failure if the descriptions for the predetermined number of correctable errors are the same.

21. (Previously presented) An apparatus for reporting failures, comprising:

detection means for detecting a predetermined number of consecutive correctable errors;

storage means for storing a description for each of the predetermined number of correctable errors;

determination means for determining whether the descriptions for the predetermined number of correctable errors are the same;

reporting means for reporting a bit line or driver failure if the descriptions for the predetermined number of correctable errors are the same; and

deconfiguration means for deconfiguring a processor if the descriptions for the predetermined number of errors are the same.

22. (Original) The apparatus of claim 21, wherein the deconfiguration means comprises means for dynamically deconfiguring the processor.

23. (Original) The apparatus of claim 21, wherein the deconfiguration means comprises means for deconfiguring the processor at boot time.

24. (Previously presented) An apparatus for reporting failures, comprising:

detection means for detecting a predetermined number of consecutive correctable errors;

storage means for storing a description for each of the predetermined number of correctable errors;

determination means for determining whether the descriptions for the predetermined number of correctable errors are the same;  
reporting means for reporting a bit line or driver failure if the descriptions for the predetermined number of correctable errors are the same; and  
means for replacing a processor if the descriptions for the predetermined number of correctable errors are the same.

25. (Canceled)

26. (Canceled)

27. (Canceled)

28. (Canceled)

29. (Canceled)

30. (Previously presented) An apparatus for reporting failures, comprising:  
a processor; and  
a memory, coupled to the processor, having stored therein an error data structure, wherein the processor detects a predetermined number of consecutive correctable errors, stores a description for each of the predetermined number of correctable errors in the error data structure, determines whether the descriptions for the predetermined number of correctable errors are the same, and reports a bit line or driver failure if the descriptions for the predetermined number of correctable errors are the same, wherein each description comprises an address at which an error occurred and an error signature that indicates which bit is bad.

31. (Canceled)